DESIGN REVIEW AND APPROVAL CHECKLIST

GENERAL INFORMATION

System design name: Model number:

Date received:

Organization requesting design review:

Contact Name:

Street Address:

City: State: Zip: Country:

Phone: ( ) Fax: ( )

Email:

1. SYSTEM DESIGN DOCUMENTATION

Check boxes for items below that are complete and acceptable.

- ☐ 1.01 Electrical drawings
- ☐ 1.02 Mechanical drawings
- ☐ 1.03 Site description and component locations (as applicable)
- ☐ 1.04 System specifications
- ☐ 1.05 Parts list
- ☐ 1.06 Source list
- ☐ 1.07 Installation and checkout procedures
- ☐ 1.08 Owners manual
- ☐ 1.09 Maintenance and troubleshooting instructions
- ☐ 1.10 System warranty
- ☐ 1.11 Component warranties

2. ELECTRICAL DESIGN

Check boxes for items below that are clearly defined and acceptable.

- ☐ 2.01 DC wiring type
- ☐ 2.02 DC wiring size
- ☐ 2.03 Acceptable DC voltage losses
- ☐ 2.04 AC wiring type
- ☐ 2.05 AC wiring size
- ☐ 2.06 Wiring terminations
- ☐ 2.07 Overcurrent protection
- ☐ 2.08 Disconnect locations and hardware
- ☐ 2.09 Grounding of conductors
- ☐ 2.10 Grounding of metal non-conductors
- ☐ 2.11 Bypass diodes
- ☐ 2.12 Blocking diodes
- ☐ 2.13 Surge and lightning protection
- ☐ 2.14 Instrumentation
- ☐ 2.15 Junction box: locations and ratings

3. MECHANICAL DESIGN

Check boxes for items below that are acceptable.

- ☐ 3.01 Array mounting design
- ☐ 3.02 Mechanical strength and compatibility with local code requirements
- ☐ 3.03 Environmentally compatible materials
- ☐ 3.04 Mounting hardware compatibility
- ☐ 3.05 Weathersealing materials and approach
- ☐ 3.06 Accessibility for array maintenance
- ☐ 3.07 Safety
- ☐ 3.08 Aesthetics

4. MODULES / ARRAYS

Check boxes for items below that meet referenced standards and are acceptable.

- ☐ 4.01 IEEE Standard 1262
- ☐ 4.02 UL Standard 1703
- ☐ 4.03 Panel, sub-array, array wiring
- ☐ 4.04 Array DC voltage
- ☐ 4.05 Bypass diode locations and sizes
- ☐ 4.06 Module performance at STC: $I_{sc,}$ $V_{oc,}$ $I_{mp,}$ $V_{mp,}$ P_{max}
- ☐ 4.07 Module performance at SOC: $I_{sc,}$ $V_{oc,}$ $I_{mp,}$ $V_{mp,}$ P_{max}
- ☐ 4.08 Predicted array output at STC: $I_{sc,}$ $V_{oc,}$ $I_{mp,}$ $V_{mp,}$ P_{max}
- ☐ 4.09 Predicted array output at SOC: $I_{sc,}$ $V_{oc,}$ $I_{mp,}$ $V_{mp,}$ P_{max}

5. INVERTER

Check boxes for items below that meet referenced standards and are acceptable.

- ☐ 5.01 UL Standard 1741
- ☐ 5.02 Location, weather-proofing
- ☐ 5.03 DC input voltage
- ☐ 5.04 Inverter power rating and operating range

6. UTILITY INTERCONNECTION

Check boxes for items below that meet referenced standards and are acceptable.

- ☐ 6.01 IEEE Standard P929
- ☐ 6.02 Interconnection agreement with utility (as applicable)
- ☐ 6.03 Location of point of connection to the utility grid

RECOMMENDATION FOR SYSTEM APPROVAL

Check the appropriate box below.

- ☐ Approve system design
- ☐ Do not approve system design (If system design not approved, specify reasons for rejection below:)

SYSTEM REVIEWER INFORMATION

Name:

Organization:

Signature: Date:

Please list other committee members reviewing this design:

Name	Organization